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** CONTINUING DATA ***** AC

This application is a CON of 10/355,301 01/31/2003 PAT 6,784,116 AC
 which is a CON of 10/013,454 12/13/2001 PAT 6,528,403 AC
 which is a CON of 09/773,000 01/31/2001 PAT 6,503,819 AC
 which is a CON of 09/086,568 05/29/1998 PAT 6,197,702 AC

** FOREIGN APPLICATIONS ***** AC

JAPAN 9-142315 05/30/1997 AC

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 06/26/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 22	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
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TITLE

Fabrication process of a semiconductor integrated circuit device